eDisconnect Switch configuration tool

Title continued

About this document

# Scope and purpose

# This document provides a user guide for setup and usage of the configuration tool to operate the eDisconnect Switch Board.

# Intended audience

eDisconnect Switch reference solution user

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# Evaluation Board Description

## Concept of the evaluation board

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1. Example figure

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1. Example figure

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# Getting Started

## Hardware Setup

1. 48V DC power supply or Battery Pack.
2. Electronic Load
3. The reference board eDisconnect Switch.
4. KIT\_XMC\_LINK\_SEGGER\_V1

## Software Setup

For the evaluation of the eDisconnect Switch Reference Boards the following software tool chain in MS window is required:

* Segger J-Link Driver
* XMCFlasher installed in Infineon Developer Center
* Micro Inspector Pro installed in Infineon Developer Center

## Getting Started Steps

1. Connect the KIT\_XMC\_LINK\_SEGGER\_V1 to X4 connector of the reference board.
2. Supply the 48V to the +BAT and -BAT.
3. Load the eDisconnect\_GUI\_G2M.wspx in the Micro Inspector Pro

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1. Example figure
2. Choose the corresponding HW target device in Micro Inspector Pro

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1. Example figure
2. Run the Micro Inspector Pro.

References

1. [Author/editor/responsible entity]: [Document title (edition information\*)]; [City of publication]\*; [Publisher]\*; [Publication date]\*; [URL]\*

Glossary

**AA**  
*active authentication (AA)*

**AAUI**  
*application activation user interface (AAUI)*

**AC**  
*alternating current (AC)*

**AC**  
*access control (AC)*

**ACLB**  
*advanced contactless bridge (ACLB)*  
The ACLB interface is used to connect the security controller acting in the role of contactless IC (CIC) to an external analog contactless frontend (ACF), both together forming (part of) a “Boosted NFC” system. The data exchange via the ACLB interface is based on symbol level.

**BDT**  
*buffered data transfer (BDT)*  
The controller is in a low-power state while a communication peripheral transfers data to or from an I/O buffer.

**BER**  
*basic encoding rules (BER)*

**BIST**  
*built-in self-test (BIST)*  
A mechanism that permits the chip to test itself.

Etc.

Revision history

| Document revision | Date | Description of changes |
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Disclaimer